

### **Features**

- Small outline 6-pin SOT23 package
- Primary-side current sensing and regulation without an opto-coupler
- Wide AC input range from  $85V_{AC}$  to  $265V_{AC}$
- High Power Factor of >0.9 without additional circuitry
- Accurate constant current ( $<\pm3\%$ )
- Low start-up current which reduces power dissipation
- Full protection functions for enhanced safety:
- · Gate driver output voltage clamp
- VCC over voltage protection VCC OVP
- VCC under-voltage lockout with hysteresis VCC UVLO
- Output LED string over current protection
- Output LED string short / open protection
- On-chip over temperature protection OTP

## Applications

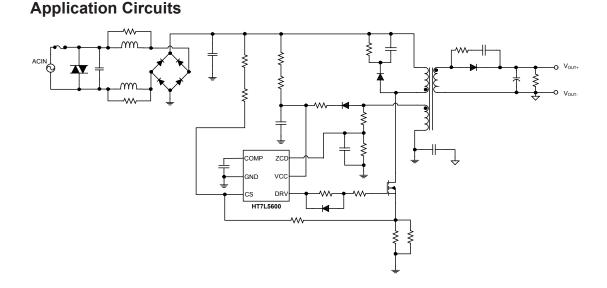
- General illumination
- E26/27, T5/T8 LED Lamp
- Other LED Lighting Applications

## **General Description**

The HT7L5600 is a single-stage, isolated, primaryside offline LED lighting controller that achieves a high power factor. Power control is implemented by controlling an external MOSFET and accurate LED constant current regulation from the primary-side information. This can significantly simplify the LED lighting system design by eliminating the secondaryside feedback components and the normally required opto-coupler. The extremely low start-up current and quiescent current reduces the total power consumption to provide a high efficiency solution for lighting applications.

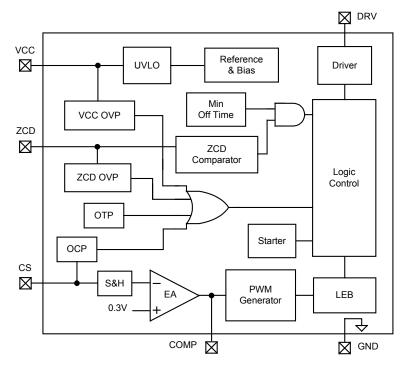
The HT7L5600 provides several protection functions, which include VCC Under Voltage Lockout (UVLO), Over Current Protection (OCP), Output LED String Open Protection, Output LED String Short Protection, VCC Over Voltage Protection (OVP) and Leading-Edge Blanking (LEB) for current sensing. Additionally and to ensure system reliability, the device includes a fully integrated thermal protection function. To protect the external power MOSFET from being damaged by an over voltage, the device DRV pin voltage is clamped to about 16V.

The high level of functional integration minimises the external component count giving major advantages in terms of cost and circuit board area. The device is supplied in a 6-pin SOT23 package.

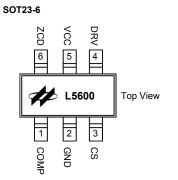




# **Block Diagram**



# **Pin Assignment**





# **Pin Description**

Pin No.	Symbol	Description	
1	COMP	Loop compensation pin. A capacitor should be placed between COMP and GND.	
2	GND	Ground pin	
3	CS	Current sense pin. A resistor is connected to sense the MOSFET current.	
4	DRV	Gate drive output for driving external power MOSFETs	
5	VCC	Power supply pin	
6	ZCD	Connected to a resistor divider from the auxiliary winding to sense the output voltage.	

# Absolute Maximum Ratings

Parameter	Range	
VCC supply voltage	-0.3V~27V	
Input voltage to CS pin	-0.3V~6V	
Output voltage at COMP pin	-0.3V~6V	
Maximum current at ZCD pin	3mA (source), 3mA (sink)	
Maximum operating junction temperature	150°C	
Storage temperature range	-55°C~150°C	

# **Recommended Operating Ranges**

Parameter	Range	
VCC Supply voltage	10V~20V	
Operating junction temperature	-40°C~125°C	

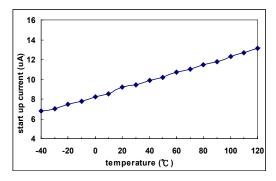


## **Electrical Characteristics**

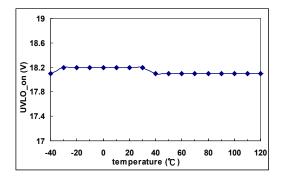
	(V <sub>cc</sub> =12V, Ta=2					
Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
Power Su	pply (VCC Pin)					
VCC <sub>ON</sub>	UVLO <sub>ON</sub>	—	—	18	—	V
VCC	UVLO <sub>OFF</sub>	—	—	7.5	—	V
VCC <sub>HYS</sub>	UVLO Hysteresis	_	10	—	—	V
V <sub>OVP1</sub>	VCC OVP Trip Point	—	21.5	24	26.5	V
I <sub>START</sub>	Start-up Current	Before turn-on, @V <sub>cc</sub> =UVLO <sub>oN</sub> -1V	_	10	20	μA
Ι <sub>Q</sub>	Quiescent Current	No switching	_	0.7	1	mA
I <sub>cc</sub>	Operating Current	@70kHz, Co=1nF		1.3	2	mA
Error Amp	blifier					
V <sub>FB</sub>	Feedback Reference Voltage	Ta=25°C	291	300	309	mV
Current S	ense Comparator					
t <sub>LEB</sub>	Leading Edge Blanking Time	_	_	400	_	ns
V <sub>OCPH</sub>	V <sub>cs</sub> Threshold Voltage for High OCP Level	V <sub>ZCD</sub> ≥0.4V	_	1.55	_	V
V <sub>OCPL</sub>	V <sub>cs</sub> Threshold Voltage for Low OCP Level	V <sub>ZCD</sub> <0.4V	_	0.4	_	V
	ent Detector		1			
V <sub>ZCDH</sub>	Upper Clamp Voltage	I <sub>ZCD</sub> =2.5mA	_	6.1	_	V
VZCDL	Lower Clamp Voltage	I <sub>ZCD</sub> =-2.5mA	_	-0.7	_	V
V <sub>ZCDA</sub>	Positive-Going Edge	_	_	150	_	mV
V <sub>ZCDT</sub>	Negative-Going Edge	_	_	50	_	mV
V <sub>OVP2</sub>	ZCD pin OVP Level	_	2.88	3.2	3.52	V
t <sub>B_OVP</sub>	OVP Detection Blanking Time	_	_	1	_	μs
Starter			1			
t <sub>start</sub>	Start Timer Period	_	_	40	_	μs
t <sub>OFF</sub>	Minimum Off Time	_	_	6.4	_	μs
Over Tem	perature Protection			1		
OTP	Over Temperature Trip Point	_	_	150	_	°C
Gate Drive	er					
t <sub>R</sub>	Rising Time	C <sub>LOAD</sub> =1nF, 10%~90%	—	50	_	ns
t <sub>F</sub>	Falling Time	C <sub>LOAD</sub> =1nF, 10%~90%	_	50	_	ns
I <sub>Source</sub>	Source Current	—	_	300	_	mA
I <sub>Sink</sub>	Sink Current	—	_	300	—	mA
V <sub>G_CLAMP</sub>	Gate Clamp Voltage	@ V <sub>cc</sub> = 20V	_	16	19.5	V

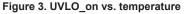


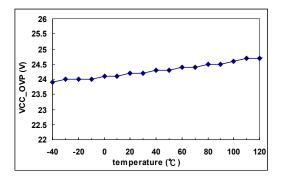
### **Typical Performance Characteristics**













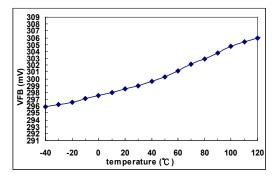


Figure 7. VFB vs. temperature

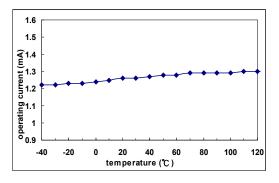


Figure 2. Operation current vs. temperature

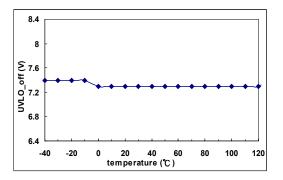


Figure 4. UVLO\_off vs. temperature

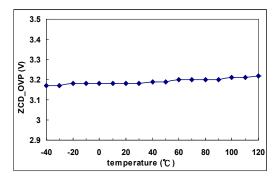


Figure 6. ZCD\_OVP vs. temperature

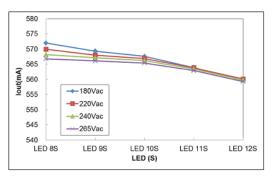


Figure 8. lout vs. LED Series



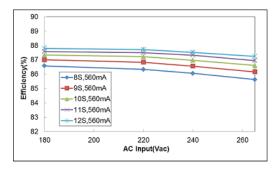


Figure 9. Efficiency vs. AC Voltage

## **Functional Description**

The HT7L5600 is a single-stage primary-side offline LED controller designed for isolated LED lighting applications. The device can achieve high Power Factor values and low THD values without resorting to additional circuits and can also generate high accuracy LED drive currents with very few external components.

#### Start-up Current

A very low start-up current,  $I_{\text{START}}$ , allows users to select a larger start-up resistor value which reduces power dissipation.

#### **Power Factor Correction**

High power factor is achieved by constant on-time operation. To implement constant on-time control, a  $0.47\mu$ F capacitor is placed between the COMP pin and ground.

#### **Constant Current Control**

The HT7L5600 accurately regulates the LED current by sensing the primary-side information. The LED current can be set as follows:

$$I_{OUT} \approx \frac{1}{2} \times \frac{V_{FB}}{R_{CS}} \times \frac{N_P}{N_S}$$

Where  $N_P$  is the primary winding and  $N_S$  is the secondary winding;  $V_{FB}$ (=300mV) is the internal voltage reference and  $R_{CS}$  is the external current sensing resistor.

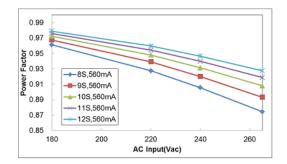


Figure 10. Power Factor(PF) vs. AC Voltage

#### VCC Under Voltage Lockout – UVLO

The device has an integrated UVLO function which includes 10V of hysteresis. The PWM controller will switch on when the VCC voltage exceeds 18V. It will switch off when the VCC voltage is less than 7.5V. The hysteresis characteristics will ensure that the device can be powered by an input capacitor during start-up. When the output voltage increases to a certain value after start-up, VCC will be charged by an output through an auxiliary winding.

#### **Boundary Conduction Mode – BCM**

The power MOSFET is turned on by inductor current zero-crossing detection. The current zero-crossing can be detected by a ZCD voltage. When the inductor current is at the zero crossing point, the voltage on the ZCD pin will drop rapidly. The HT7L5600 then detects the falling edge and turns on the Power MOSFET. The boundary conduction mode provides low turn-on switching losses and high conversion efficiency.

#### Leading-Edge Blanking – LEB on CS

At each turn on time of the external power MOSFET, a spike will inevitably be generated at the sense resistor. To prevent faulty triggering, a 400ns leading-edge blank time will be generated. During this blanking period, the current-limit comparator is disabled and will therefore not be able to switch off the gate driver.

#### **Gate Driver Clamp**

The DRV pin is connected to the gate of external MOSFET to switch it on and off. To protect the external power MOSFET from being over-stressed, the gate driver output is clamped to 16V.



#### **Over Voltage Protection – OVP on VCC**

In order to prevent PWM controller damage, the device includes an OVP function for VCC. Should the VCC voltage exceed the OVP threshold voltage of 24V, the PWM controller will cease operation immediately. When the VCC voltage falls below the UVLO off level, the controller will reset.

#### LED Open Protection – ZCD OVP

The ZCD pin voltage is set by a resistor divider  $R_{TOP}$  (top resistor),  $R_{BOT}$  (bottom resistor) and an auxiliary winding due to the coupling polarity between the auxiliary winding and the secondary winding of the transformer. Once the ZCD voltage exceeds 3.2V after a blanking time about 1us to allow the leakage inductance ringing to be fully damped, ZCD OVP is triggered. The device will then stop switching but it can be reset by re-starting the voltage on the VCC pin. The OVP voltage can be adjusted by the equation:

$$V_{OUT\_OVP} = 3.2 \times (1 + \frac{R_{TOP}}{R_{BOT}}) \times \frac{N_s}{N_A} + V_D$$

Where  $N_s$  is the secondary winding,  $N_A$  is the auxiliary winding and  $V_D$  is the forward bias voltage of the secondary diode.

#### **Over Current Protection – OCP**

The HT7L5600 includes a CS pin over current protection function. An internal circuit detects the current level and should the current be larger than the over current protection threshold level, the gate output will then be fixed to a low level.

#### **LED Short Protection – SCP**

When an LED short condition occurs, the SCP function can prevent power components from being damaged due to high currents. As shown in Figure 11, the device determines whether the LED is in a short circuit condition by detecting the voltage on the ZCD pin when the DRV output is low. If the detected voltage is lower than 0.4V, the device will set the low OCP detection voltage on the CS pin to  $V_{OCPL}$  (0.4V). When this happens, the peak current flowing through the

primary side MOSFET will be limited to V<sub>OCPL</sub>/R<sub>CS</sub>, thus maintaining a lower current to protect the power components. If the detected voltage is equal to or higher than 0.4V, the device will set the OCP detection voltage to V<sub>OCPH</sub> (1.55V). When this happens the peak current will be limited to V<sub>OCPH</sub>/R<sub>CS</sub>, which should be higher than the peak current under normal operation to avoid the LED current falling below the set value for normal operation.

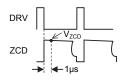


Figure 11. ZCD Signal Sensing

As shown in Figure 12, after the AC power is turned on under normal operation, the V<sub>CC</sub> voltage will be charged by the start-up resistor until the UVLO function is on. After this happens the device can then be activated. The initial value of  $V_{\mbox{\tiny ZCD}}$  is lower than 0.4V and the OCP detection voltage on the CS pin is  $V_{OCPL}$ . When the  $V_{ZCD}$  voltage rises to a value equal to or greater than 0.4V, the device determines that the LED is not in a short circuit condition and sets the OCP detection voltage to V<sub>OCPH</sub>. When an LED short circuit condition occurs under steady normal operation, the  $V_{\mbox{\tiny LED}}$  voltage tends to 0V, and the  $V_{\mbox{\tiny CC}}$ voltage drops. It should be noted that although the V<sub>ZCD</sub> voltage is lower than 0.4V, the OCP detection voltage set as  $V_{\text{OCPH}}$  will remain unchanged. When the UVLO On/Off restarts and the V<sub>ZCD</sub> voltage remains lower than 0.4V, the device will detect an LED short circuit condition. When this happens the OCP detection voltage will maintain the initial setting of  $V_{OCPI}$ , and consequently the peak current of  $V_{OCPI}/R_{CS}$ will be lower than the normal operating current. Now the device will continuously enter the UVLO On/ Off restart state. In such cases, the components can be protected from being damaged by the continuous flow of high current. When the LED short circuit condition ceases to exist, the  $V_{\mbox{\scriptsize ZCD}}$  voltage will rise to a value equal to or greater than 0.4V. The device will then determine that the LED is not in a short circuit condition and the OCP detection voltage on the CS pin will resume to V<sub>OCPH</sub> for normal operation.



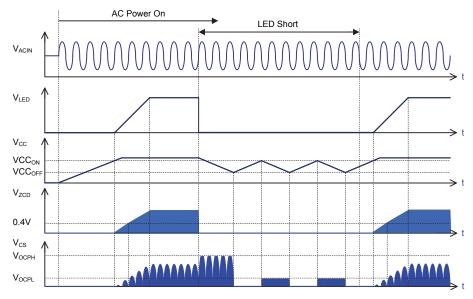


Figure 12. Waveforms in LED Short Condition

#### **Thermal Protection**

A thermal protection feature is included to protect the device from excessive heat damage. When the junction temperature exceeds a threshold of 150°C, the thermal protection function will turn off the DRV terminal immediately. When the VCC decreases below the UVLO off level, the controller will reset.



# **Package Information**

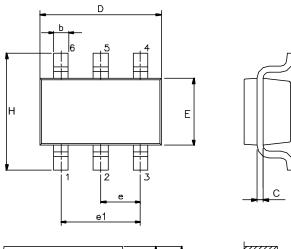
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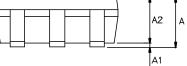
Additional supplementary information with regard to packaging is listed below. Click on the relevant section to be transferred to the relevant website page.

- Further Package Information (include Outline Dimensions, Product Tape and Reel Specifications)
- Packing Meterials Information
- Carton information



## 6-pin SOT23 Outline Dimensions







Symbol	Dimensions in inch			
Symbol	Min.	Nom.	Max.	
A	_	—	0.057	
A1	_	_	0.006	
A2	0.035	0.045	0.051	
b	0.012	—	0.020	
С	0.003	_	0.009	
D	_	0.114 BSC	—	
E	_	0.063 BSC	—	
е	_	0.037 BSC	—	
e1	_	0.075 BSC	—	
Н	_	0.110 BSC	—	
L1	_	0.024 BSC	—	
θ	0°	—	8°	

Symbol	Dimensions in mm			
Symbol	Min.	Nom.	Max.	
A	—	—	1.45	
A1	—	—	0.15	
A2	0.90	1.15	1.30	
b	0.30	_	0.50	
С	0.08	_	0.22	
D	_	2.90 BSC	—	
E	_	1.60 BSC	_	
е	_	0.95 BSC	_	
e1	_	1.90 BSC	—	
Н	_	2.80 BSC	_	
L1	_	0.60 BSC	—	
θ	0°	_	8°	

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