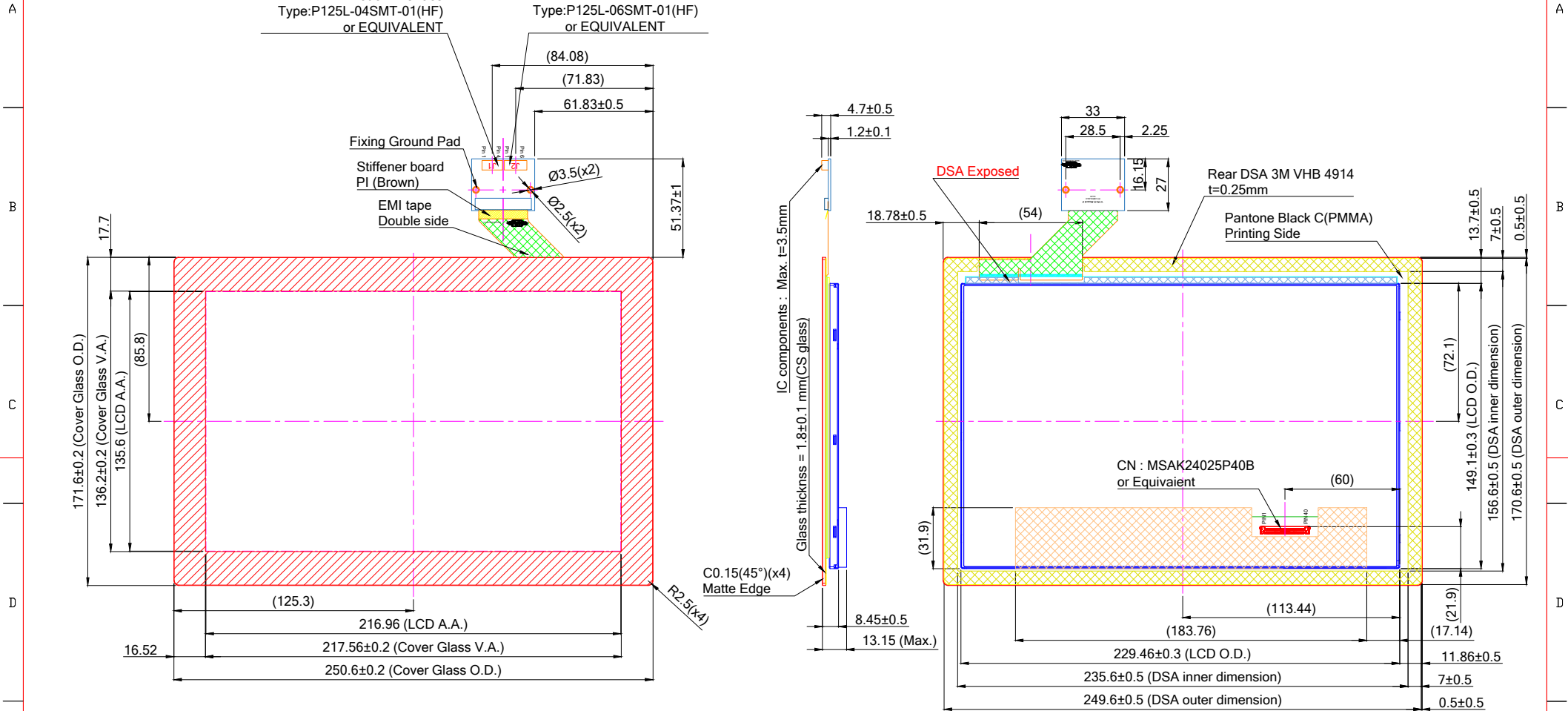


Front VIEW

Back VIEW



Note:

1. Tolerance: ±0.5mm
2. Touch Surface Hardness: 6H (C.S.)
3. Touch Transmittance: >85% (JIS-K7105)
4. Touch Surface Treatment: None
5. Referring to the integration guide to avoid any integration noise issue
6. Assembly Solution :DSA

LVDS Connector Pin Define

PIN	NAME	PIN	NAME	PIN	NAME	PIN	NAME
1	NC	11	Rxin1-	21	Rxin3+	31	LED_GND
2	VDD	12	Rxin1+	22	GND	32	LED_GND
3	VDD	13	GND	23	NC	33	LED_GND
4	NC	14	Rxin2-	24	NC	34	NC
5	NC	15	Rxin2+	25	GND	35	PWM
6	SEL 6/8bit	16	GND	26	NC	36	BL_EN
7	NC	17	RxCLK-	27	NC	37	NC
8	Rxin0-	18	RxCLK+	28	GND	38	LED_VCC
9	Rxin0+	19	GND	29	NC	39	LED_VCC
10	VDD(5V)	20	Rxin3-	30	NC	40	LED_VCC

J2:Pin Define for I2C 6 pin

Pin 01	GND
Pin 02	SDA
Pin 03	SCL
Pin 04	VDD(3.3V) (2.2V Max)
Pin 05	INT
Pin 06	RST

J1:Pin Define for USB 4 pin

Pin 01	GND
Pin 02	D-
Pin 03	D+
Pin 04	VDD(5V)

Customer Approval		Part Number #Rev		FLD-101HMLG2PC004 #01	
Date	Draw Ver	Date	Person	Description	
Company	Name				
Signature	Date	Design By	Date	Approved By	
	First Drawing	20240503	Leo	20240503	Kevin W

